

Name: Mrs. Deshpande Priya Sandeep



#### Contact Details:

- 1) Email id: psd\_entc@pvgcoet.ac.in
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#### Educational Qualification:

- 1) ME (Electronics & telecommunication), University of Pune (SPPU), Pune, 2011
- 2) BE (Electronics & Telecommunication), Shivaji University, Kolhapur (SPPU), Pune, 2003

#### Research Interests:

VLSI Design & Technology, VLSI in signal processing

#### Field of Consultancy (Aspiring): -

#### Experience:

- 1) Teaching Experience (total number of years): 11 years

#### **Publications:**

##### **1) International Conferences –**

- 2011 IEEE International Conference on Computational Intelligence and Computing Research, 15<sup>th</sup> December 2011 to 18<sup>th</sup> December 2011 at CAPE Institute, Kanyakumari.

**Title:** High Throughput Subpipelined Implementation of AES algorithm On FPGA

##### **2) National Conferences**

- National Conference on “Information & Communication Technology,26th May 2011 to 27thMay2011 at RSCOE, Pune

**Title:** AES Implementation on FPGA

National conference on “Futuristic Applications in Electronics Engineering” 10th March 2011 to 11th March 2011 at AISSMS IOIT,Pune

Title: Review Of AES Implementation Strategies on FPGA

**Professional Society Memberships:**

**ISTE : membership Number:** LM 67308

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