

Pune Vidyarthi Griha's
COLLEGE OF ENGINEERING & TECHNOLOGY, PUNE 9

Time Table with effect from 1 JAN 2018 to End of SEM II

Class: SE IT

Academic Year: 2017-18


Term: II


Class Teacher: Prof. S.A.Deshpande


TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
08.30-09.30						S1:PIL S2:DSFL
09.30-10.30						S3:PIL S4:CGL
10.30-11.30	S1:PIL S2:CGL	S1:CGL S2:DSFL	S1:DSFL S2:PIL	S1:CGL S2:PIL	S1:DSFL S2:CGL	MII Extra Lecture for Diploma Students 409
11.30-12.30	S3:DSFL S4:DSFL	S3:CGL S4:PIL	S3:CGL S4:PIL	S3:DSFL S4:CGL	S3:PIL S4:DSFL	
12.30-01.30						
01.30-02.30	FCCN 409	MIII 409	PAI 409	FCCN 409	MIII 409	
02.30-03.30	M III -TUT 409/311	FCCN 409	MIII 409	MIII 409	DSF 409	
03.30-04.30	CG 409	PAI 409	FCCN 409	DSF 409	CG 409	
04.30-05.30	DSF 409	CG 409	DSF 409	PAI 409	PAI 409	


SUB	STAFF
M III -Mathematics III	Prof.Rajeev Singh/Prof .J.M.Mankar
CG -Computer Graphics	Prof. S. A. Deshpande
PAI -Processor Architecture and Interfacing	Prof. D. T. Varpe
DSF-Data Structure and Files	Prof. S.A.Mulay
FCCN- Foundations of Communication and Computer Network	Prof. S.G.Kamble

SUB	STAFF	LAB
Data Structure and Files Lab-(DSFL)	S1,S2,S3: Prof. S.A.Mulay	LAB 3(301)
	S4: Prof. S.A.Mulay / Dr.S.A.Mahajan	LAB 2(301)
Processor Interfacing Lab- (PIL)	S1,S2:Prof. D. T. Varpe / Prof. M.R.Apsangi	LAB 4(302)
	S3, S4:Prof. D. T. Varpe	LAB 5(302)
Computer Graphics Lab (CGL)	S3,S4:Prof. S. A. Deshpande	LAB 2(301)
	S2 :Prof. S. A. Deshpande	LAB 1(312)
	S1 : Prof. M.R. Mahajan	LAB 1(312)
MIII(Tut)	S1,S2,S3,S4	409/311


Time Table
In charge
Prof. M.R.Apsangi


Time Table
Coordinator
Prof. A.D. Purandare
Prof. O.S. Pawaskar


HOD
Information Technology
Prof. S. S. Dixit


Principal
Dr. Y. P. Nerkar