

**PUNE VIDYARTHI GRIHA'S  
COLLEGE OF ENGINEERING AND TECHNOLOGY, PUNE  
Department of Electronics and Telecommunication**

**Prasanna Gopal Shete**



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## BIO DATA

Full Name : **Prasanna Gopal Shete**

Highest Educational Qualification : M. Tech in Electronics  
(Digital Systems)

**PhD (Pursuing) in SPPU**

Permanent address : Flat No. 401, Shree Gajannan Geet,  
Near Mahalaxmi Mandir, Shivdarshan,  
Pune\_411009

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(O) 24228258/65/79

E-mail id : shetepg@gmail.com

Current designation : Head, E &TC department

Total Working Experience : 25 Years

Patent : **First Published on 17<sup>th</sup> July 2015.**  
**Second Published on 28<sup>th</sup> April 2017.**

No. of Research papers : Papers 12

International Journal: 03, International Conference: 06, National Conference: 06

Books published : Books – 01

Number of Conferences/ Workshops / STTPs conducted : 10+

University Project Competition: **AVISHKAR 13 STATE** Inter-University Research  
Convention, at North Maharashtra University, 16<sup>th</sup> to 18<sup>th</sup> January 2014.

University of Pune **BCUD Research grant**: Rs 1,30,000/- (2013-2015)

Syllabus workshops: **12**, Technical Seminar /Workshops: **21**

## EDUCATIONAL QUALIFICATION

Degree	Specialization	Year of passing	Institute/University	Percentage
PhD (E &TC)	Bio-Medical Signal Processing	Pursuing	Matoshree College of Engineering & Research Centre, Nasik. Savitribai Phule Pune University	
M.Tech (Digital Systems)	Electronics and Telecommunication	2006	PIET's College of Engineering, UoP	CGPA 7.92
B.E	Electronics	1991	Walchand Institute of Technology- Sholapur Kolhapur University	70.00 %

## EMPLOYMENT HISTORY

College	Designation	Year [Total Experience]
Pune Vidyarthi Griha's College of Engineering Pune-09	Head, Assistant Professor in E&TC dept.	1 <sup>st</sup> August 2007 to till date Ref.No./CCO/Approval/273 Ref.No./CCO/Approval/275 Ref.No./CCO/Approval/35
Pune Vidyarthi Griha's College of Engineering Pune-09	Lecturer in E&TC dept.	1 <sup>st</sup> July 1994 to 31 <sup>st</sup> July 2007 Ref.No./CCO/Approval/167
S.S.V.P.S College of Engineering Dhule-05	Lecturer in E&TC dept.	1 <sup>st</sup> August 1991 to 28 <sup>th</sup> July 1994

## RECOGNITIONS / APPRECIATIONS/PATENT FILED

	<b>Title</b>	<b>Awarded by</b>	<b>Date</b>	<b>Remarks</b>
Patent Filed	“Innovative Breast Cancer Prognosis detection based on HER2 marker”	Govt. of India Mumbai	28 <sup>th</sup> April 2017	Research Topic
Patent Filed	“Digital Signal Spectrum System”	Govt. of India Mumbai	17 <sup>th</sup> July 2015	17 <sup>th</sup> July 2015 (Project in 2013)
Certification	CICTT Certification Mission 10 X	Wipro Pvt. Ltd and Cambridge Uni. UK	June 2011	Innovative Teaching in Engineering
	Newsletter for Mission 10X Nov.10	Wipro Pvt. Ltd.	November 2010	Innovative Teaching in Engineering

## RESEARCH PAPERS PUBLISHED IN REFERRED JOURNALS

<b>Publication</b>	<b>Title of paper</b>	<b>Year of Publication</b>
ICCCC '17 International Conference	Evaluation of IHC-ER and HER2 in Breast Cancer	18 <sup>th</sup> & 19 <sup>th</sup> August 2017
ICTACT '2016, International Conference	Evaluation of IHC marker HER2 in Breast Cancer	August 2016
ICBBS ' 2015 , International Conference	Evaluation of IHC biomarker for BC detection	25 <sup>th</sup> , 26 <sup>th</sup> June 2015
PISER , International Journal – ISSN 2347- 6680 (E)	A Review of Breast Cancer cell Evaluation Technique and Biomarkers	June 2014
IJSTR – ISSN 2277- 8616	Android based Spectrum Analyzer	October 2013
CiiT, International Journal	ER cell Detection in Breast Cancer Using Modified Watershed Algorithm	December 2012.
IJERT, International Journal	Breast Cancer Cell Detection Using Image Processing	November 2012.

**NATIONAL AND INTERNATIONAL CONFERENCES / WORKSHOPS / STTPS  
CONDUCTED / ATTENDED**

**TECHNICAL WORKSHOP/STTP/SEMINARS**

<b>Name of the course</b>	<b>Place</b>	<b>Duration</b>
MATLAB EXPO 17	J W Marriott, Pune	25 <sup>th</sup> April 2017
TI Embedded system design using TIVA	PVG's COET, Pune	15 <sup>th</sup> to 17 <sup>th</sup> December 2016
Signals and Systems	PVG's COET, Pune	2 <sup>nd</sup> January to 10 <sup>th</sup> January 2014
Automotive Electronics	PVG's COET, Pune	May 2012
MATLAB in Engineering Education	Le Meridian Pune	14 <sup>th</sup> March 2012
"Technical Report Writing using LaTeX"	PVG's COET Pune	24 <sup>th</sup> ,25 <sup>th</sup> Feb.2012
"Writing Effective Conference Papers"	PVG's COET (Videoconferencing)	18 <sup>th</sup> ,19 <sup>th</sup> Feb.2012
"DSP Virtual Laboratory on DSP Processor 67XX"	IIT Bombay	13,14 Dec.2010
"Integrated Inertia for Navigation and GSM "	PVG's COET Pune	03 <sup>rd</sup> Nov.2010
"FPGA and its applications" at COE Pune	PVG's COE Pune	7 <sup>th</sup> June to 18 <sup>th</sup> June 2010 (Two weeks)
Multi discipline Research in Engineering	MAE Alandi Pune	9 <sup>th</sup> July 2010
Speech Signal Processing	AISSM IOIT Pune-05	25 <sup>th</sup> Feb.2010

## CONTRIBUTIONS AT UNIVERSITY LEVEL:

### SYLLABUS WORKSHOPS

Year	Name of the college	Duration	Role Played
SE (ETC) Syllabus FoW, BoS, SPPU	D.Y. Patil COE, Akurdi, Pune	December 2016	SE - IC
SE (ETC) Syllabus	MES Wadia, Pune	June 2015	SE – EDC
F.E (ETC) Syllabus	SITS, Narhe, Pune	04 <sup>th</sup> August 2012	BElexE Subject Expert
F.E (ETC) Syllabus	AISSMS COE Pune	06 <sup>th</sup> March 2012	BEE Syllabus Reformation
B.E (E&TC) syllabus Restructure	BSCOE & R, Narhe Pune	25 <sup>th</sup> March 2011	Subject member
T.E (E&TC) syllabus Detailing	MMIT, Lohgaon, Pune	13 <sup>th</sup> July 2010	Subject member
S.E (E&TC) Syllabus Detailing.	SGR College Engg. & Management. Ahmednagar	12 <sup>th</sup> January 2010	Subject member AIC

### WORKED - EXAMINER / CHAIRMAN/ FOR BOARD OF STUDIES

Name of the course	Place	Duration
FE (E&TC) – Basic Electronics Engineering - Chairman	University of Pune	May 2013
SE (E&TC) – Electronics Devices and circuits (EDC) - Chairman	University of Pune	May 2012
F.E Online Exam 2010-11 Core Committee member	University of Pune	May 2011
F.E Online Exam Senior Supervisor	University of Pune at VIIT Pune	11 <sup>th</sup> Jan.2011
Custodian for UoP	University of Pune	17 May to 28 <sup>th</sup> May 2010

## EFFORTS -IMPROVING TEACHING LEARNING PROCESS

Name of the course	Place	Duration
Electrical Workshop	PVG's COET Pune	16 <sup>th</sup> to 18 <sup>th</sup> December 2014
“Mission 10X – Teaching Methodology” at Muktangnan English School.	Muktangan English School, Pune -09.	11 <sup>th</sup> February 2012
“ Mission 10 X – Master Trainers Workshop“ at WIPRO Pvt. Ltd.	Wipro Technologies,Pune	4 <sup>th</sup> & 5 <sup>th</sup> January 2012
CICTT Certification Mission 10 X	Wipro Pvt. Ltd & Cambridge Uni. UK	June 2011
“Mission 10X “AARAMBHA” for college staff experience less than 03 years.	PVG COET, Pune	23 <sup>rd</sup> March 2011
“Mission 10X – Teaching Methodology” at Naval COE Lonavala.	Naval COE Lonavala.	29 <sup>th</sup> June 2010

## MEMBERSHIP OF PROFESSIONAL BODIES

Sr. No.	Professional body	Membership	Date
01	ISTE	Life Member LM26376	2000
02	IEEE	Member	2006,2013-14,14-15

## WORKED AS RESOURCE PERSON/WORKED AS JUDGE

Year	Subject	Name of the college	Duration
Avishkar 16	Project Competition	SPPU, VIIT, Pune	November 2016
Momentum 16	Paper Presentation Competition	Shahu COE, Pune	January 2016
SE (E&TC)	EDC	MIT COE Pune	04 <sup>th</sup> September 15
Project Conclave 15	Project Competition	COE Pune	27 <sup>th</sup> March 2015
Impetus and Concepts 15	Paper Presentation	PICT Pune	23 <sup>rd</sup> March 2015



Project Competition	DIPEX 14	VCOE, Pandharpur	04 <sup>th</sup> to 07 <sup>th</sup> March 2014
Project Competition	IEEE Communication Society	PVGCOET, Pune	24 <sup>th</sup> March 2014
Electrical Workshop	Integrated Circuits	PVG's COET Pune	16 <sup>th</sup> to 18 <sup>th</sup> December 2014
Project Competition	IEEE Comm. Society	PVGCOET, Pune	24 <sup>th</sup> March 2014
Electrical Workshop	Integrated Circuits	PVG's COET Pune	16 <sup>th</sup> to 18 <sup>th</sup> December 2014
SE (E&TC)	Electronics Devices and Circuits	Universal COER, Pune	23 <sup>rd</sup> Sept.2013
SE (E &TC)	Basics of BJT and FET/MOSFET	BSCOER, Narhe Pune	22 <sup>nd</sup> and 29 <sup>th</sup> March 2012
SE (E &TC)	'h' parameter in op-amp	PVG's COET Pune	19 <sup>th</sup> March 2012
TECHNOVISION 12	Circuit Design Competition	JSCOE Pune	28 <sup>th</sup> to 29 <sup>th</sup> February 2012
Tesla '12	Paper Presentation TESLA 12	MIT COE Pune	27 <sup>th</sup> February 2012
SE (E &TC)	Design of Voltage Regulator	PVG's COET Pune	12 <sup>th</sup> April 2011
WIT-Char Project Exhibition	Judge for Projects	WIT char Solapur	19 <sup>th</sup> & 20 <sup>th</sup> March 2011
S.E (E&TC)	Integrated circuits & Application	AISSMS IOIT COE Pune-05	25 <sup>th</sup> March 2011

### **ANY OTHER SIGNIFICANT CONTRIBUTION:**

- Worked as Local Inquiry Committee Member and Staff Selection Committee.
- Worked as Co-ordinator of Versatalia-09 in PVG's COET.
- Worked as Vice President Gymkhana in PVGCOET, Pune-09
- Worked as Assistant Director in CAP for UoP.
- Worked as Co-ordinator Entrepreneurship Development Cell and for Master Trainer for Mission10x in PVG's COET.

The above data and information is true to the best of my knowledge and is after 2010.

**Prasanna G. Shete**

**Date: 19<sup>th</sup> August 2017**