

Pune Vidyarthi Griha's
COLLEGE OF ENGINEERING & TECHNOLOGY, PUNE 9

Time Table with effect from **15 DEC 2016** to End of SEM II

Class: SE IT

Academic Year: 2016-17

Term: II

Class Teacher: Prof. S.A.Deshpande

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
08.30-09.30						S1:DSFL S2:CGL
09.30-10.30						S3:DSFL S4:PIL
10.30-11.30	S1:CGL S2:PIL	S1:PIL S2:CGL	S1:CGL S2:DSFL	S1:PIL S2:DSFL	S1:DSFL S2:PIL	
11.30-12.30	S3:PIL S4:DSFL	S3:DSFL S4:CGL	S3:PIL S4:PIL	S3:CGL S4:CGL	S3:CGL S4:DSFL	
12.30-01.30						
01.30-02.30	MIII 410	DSF 410	PAI 410	MIII 410	DSF 410	
02.30-03.30	M III –TUT 409/410	CG 410	MIII 410	CG 410	MIII 410	
03.30-04.30	FCCN 410	PAI 410	DSF 410	FCCN 410	PAI 410	
04.30-05.30	DSF 410	FCCN 410	CG 410	PAI 410	FCCN 410	

SUB	STAFF
Mathematics III – (M III)	Prof.Rajeev Singh/T.K. Mudaliyar
Computer Graphics – (CG)	Prof. S. A. Deshpande
Processor Architecture and Interfacing – (PAI)	Prof. D. T. Varpe
Data Structure and Files – (DSF)	Prof. M.R. Apsangi
Foundations of Communication and Computer Network – (FCCN)	Prof. M.R. Mahajan

SUB	STAFF	LAB
Data Structure and Files Lab-(DSFL)	S1:Prof. D.D.Sapkal	LAB 3(301)
	S2: Prof. M.R. Apsangi	LAB 3(301)
	S3,S4: Prof. M.R. Apsangi	LAB 2(301)
Processor Interfacing Lab-(PIL)	S2,S4:Prof. D. T. Varpe	LAB 4(302)
	S1, S3:Prof. D. T. Varpe	LAB 5(302)
Computer Graphics Lab (CGL)	S1,S2,S3:Prof. S. A. Deshpande	LAB 9(407)
	S4 : Prof. S.A. Mahajan	LAB 10(407)
MIII(Tut)	S1,S2,S3,S4	409/410

**Time Table
In charge
Prof. M.R.Apsangi**

**Time Table
Coordinator
Prof. A.D. Purandare
Prof. O.S. Pawaskar**

**HOD
Information Technology
Prof. S. S. Dixit**

**Principal
Dr. Y. P. Nerkar**