

**Pune Vidyarthi Griha's**  
**COLLEGE OF ENGINEERING & TECHNOLOGY, PUNE 9**

**Time Table with effect from 15 DEC 2016 to End of SEM II**

**Class: SE COMP**

**Academic Year: 2016-17**

**Term: II**

**Class Teacher: Prof .M.S.Pokale**

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
08.30-09.30						
09.30-10.30						Maths Tut (S3,S4) 409
10.30-11.30	Maths Tut (S1,S2) 409	PPL 409	MIII 409	CG 409	MIII 409	S1:MPL S2:CGL
11.30-12.30	MIII 409	ADS 409	ADS 409	MIII 409	MP 409	S3:CGL S4:ADSL
12.30-01.30						
01.30-02.30	S1:CGL S2:CGL	S1:ADSL S2:ADSL	S1:ADSL S2:MPL	S1:MPL S2:ADSL	S1:CGL S2:MPL	
02.30-03.30	S3:ADSL S4:MPL	S3:MPL S4:CGL	S3: ADSL S4:CGL	S3:CGL S4:MPL	S3:MPL S4:ADSL	
03.30-04.30	MP 409	MP 409	CG 409	ADS 409	PPL 409	
04.30-05.30	CG 409	CG 409	MP 409	PPL 409	ADS 409	

SUB	STAFF
Engineering Maths III(MIII)	Prof.Rajeev Singh / T.K. Mudaliyar
Principles of Programming Languages (PPL)	Prof. M.V. Marathe
Microprocessor(MP)	Prof. M.S. Pokale
Computer Graphics(CG)	Prof. A.M. Bhadgale
Advanced Data Structures(ADS)	Trupti Shinde

SUB	STAFF	LAB
Advanced Data Structures Lab(ADSL)	S2,S3 : Prof. M.R.Mahajan	LAB 4 (302)
	S4 : Prof. M.R.Mahajan	LAB 5 (302)
Microprocessor Lab(MPL)	S1 : Trupti Shinde	LAB 5 (302)
	S1,S2: Prof. M.S. Pokale	LAB 2 (301)
Computer Graphics Lab(CGL)	S3,S4: Prof. M.S. Pokale	LAB 3 (301)
	S1,S3 : Prof. A.M. Bhadgale	LAB 8(307)
	S4 : Prof. A.M. Bhadgale	LAB7(307)
Mathematics III (Tut)	S2 : Prof. M.V. Marathe	LAB 7(307)
	S1,S2,S3,S4	409

**Time Table**  
**In charge**  
**Prof. M. V. Marathe**

**Time Table**  
**Coordinator**  
**Prof. A.D. Purandare**  
**Prof. O.S. Pawaskar**

**HOD**  
**Computer Engineering**  
**Prof. D. A. Kulkarni**

**Principal**  
**Dr. Y. P. Nerkar**