

Pune Vidyarthi Griha's
COLLEGE OF ENGINEERING & TECHNOLOGY, PUNE-9
Department of Electrical Engineering

Time Table with effect from 27th JUNE 2016

Class: BE (ELECT)

Academic Year: 2016-2017

Term: I

Class Room: 204

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
08.30-09.30						↕ V LAB ↕
09.30-10.30						
10.30-11.30	↑ B1 – Project B2 – PSOC	↑ B1 - CS II B2 – Project	↑ B1 - RES B2 – CS II	↑ B1 – PLC B2 – PQ	↑ B1 – PSOC B2 - PLC	
11.30-12.30	B3 – PLC B4 –PQ ↓ B5 - CS II	B3 – PSOC B4 – PLC ↓ B5 – RES	B3 – Project B4–PSOC ↓ B5 – PLC	B3 – CS II B4–Project ↓ B5 - PSOC	B3 – PQ B4 – CS II ↓ B5 - Project	
12.30-01.30	EL II- IETS	EL I- PQ	EL I- PQ	EL I- PQ /RES	EL I- RES	
01.30-02.30	RECESS	EL I- RES	EL I- RES	RECESS	EL I- PQ	
02.30-03.30	PSOC	EL II- IETS	CS II	EL II- IETS	PLC	
03.30-04.30	PLC	PLC	PSOC	PSOC	CS II	
04.30-05.30	CS II	CS II	EL II- IETS	PLC	PSOC	

Time Table In-charge
Prof. O. S. Pawaskar

Time Table Coordinator
Prof. O. S. Pawaskar

HOD
Prof. Dr. Mrs. G. A. Vaidya)

Principal
(Dr. Y. P. Nerkar)