

**Pune Vidyarthi Griha's**  
**COLLEGE OF ENGINEERING & TECHNOLOGY Pune-411009**

**Time Table With effect from:27/06/2016**

**Class:BE E&TC-I**

**Academic Year : 2016-2017**

**Term:I**

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
8.30 -9.30						
9.30 - 10.30		ELECTIVE -II PLC-MPP[107]		ELECTIVE -II PLC-MPP[107]		
10.30-11.30	B1-VLSI[PSD] B2-CN[MHB] B3-MWE[YBT] B4-ESDRTOS[MMH] B5-PROJECT[]	B1-PROJECT[] B2-VLSI[PSD] B3-CN[MHB] B4-MWE[YBT] B5-ESDRTOS[MMH]	B1-DIP[PGS] B2-PROJECT[] B3-VLSI[PSD] B4-CN[MHB] B5-MWE[VYD]	B1-MWE[YBT] B2-DIP[PGS] B3-PROJECT[] B4-VLSI[PSD] B5-CN[MHB]	B1-CN[MHB] B2-MWE[VYD] B3-ESDRTOS[MMH] B4-PROJECT[] B5-VLSI[PSD]	
11.30 -12.30						
12.30 -01.30	ELECTIVE -II EPD-NPD[107] PLC-MPP[404]	VLSI- PSD[107]	MWE-YBT[107]	CN- MHB[107]	VLSI- PSD[107]	
01.30-02.30	LUNCH					
	BREAK					
02.30-3.30	CN- MHB[404]	MWE-YBT[404]	CN- MHB[404]	MWE-YBT[404]	MWE-YBT[404]	
3.30- 4.30	ELECTIVE -I DIP-PGS[404] ESDRTOS-MMH[405]	ELECTIVE -II EPD-NPD[404]	ELECTIVE -I DIP-PGS[404] ESDRTOS-MMH[405]	ELECTIVE -II EPD-NPD[404]	ELECTIVE -I DIP-PGS[404] ESDRTOS-MMH[405]	
4.30 -5.30	VLSI- PSD[107]					

**Incharge Time Table**

**Time Table Coordinator**

**Head -E & TC**

**Principal**

